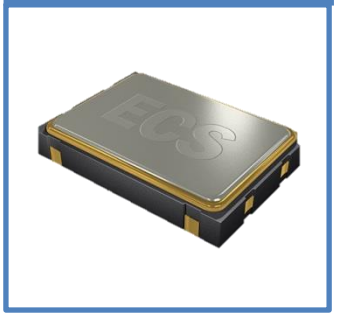
ECS-3955M Series

SMD Clock Oscillator

The ECS-3955M (5V) is a high capacitive load version of our miniature, crystal controlled low current clock oscillator in a ceramic SMD package. The low profile package is ideal for PC's, portable applications and PCMCIA cards.

Request a Sample

ECS-3955M



- High capacitive load options
- Low Power Consumption
- Tri-State Function
- Seam welded package
- Tape & Reel (1,000 pcs STD)

OPERATING CONDITIONS / ELECTRICAL CHARACTERISTICS

Parameters	Conditions	ECS-3955M (5V)			Units
		MIN	TYP	MAX	
Frequency Range		1.800		70.000	MHz
Temperature Range	Operating	-10		+70	°C
	Storage	-55		+125	°C
Supply Voltage		+4.5	+5.0	+5.5	V DC
Frequency Stability*	Standard			±100	PPM
	Option (B)			±50	PPM
	Option (C)			±25	PPM
Input Current	1.8 ~ 36.0 MHz			30	mA
	36.1 ~ 70.0 MHz			65	mA
Output Symmetry	@ ½ VCC Level	40/60	50±4	60/40	%
Rise and Fall Times			7		nS
Output Voltage	VOL			VCC x 0.1V	V DC
	VOH	VCC x 0.9V			V DC
Load	HCMOS			50	pF
Start-Up Time	1.8 ~ 36.0 MHz			5	mS
	36.0 ~ 70.0 MHz			10	mS
Output Current (IOL)	VOL			16	mA
(IOH)	VOH			-16	mA
Enable/Disable Time			100		ns

** Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging shock and vibration.*

Part Numbering Guide: Example ECS-3955M-500-B-TR

ECS -	Series -	Frequency Abbreviations -	Stability Tolerance -	Packaging
ECS	3955M (5x7mm, +5V)	500 = 50 MHz	A = ±100 ppm B = ±50 ppm C = ±25 ppm D = ±20 ppm	TR = Tape & Reel 1K/Reel

Package Dimensions (mm)

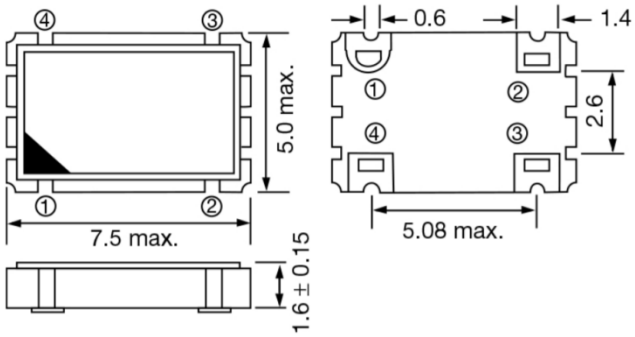


Figure 1) *Top, Side, and Bottom views*

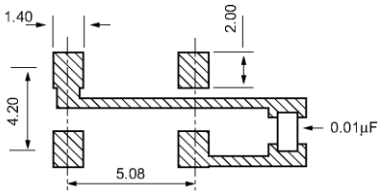


Figure 2) *Land Pattern*

Pin Connections	
#1	Tri-State**
#2	Ground
#3	Output
#4	VCC

*** An internal pullup resistor from pin 1 to 4 allows active output if pin 1 is left open.*
Note: A 0.01 µF bypass capacitor should be placed between VCC (Pin 4) and Ground (Pin 2) to minimize power line noise.

ECS-3955 Standby Control Voltage	
Pin #1 = Open	#3 = Oscillation
Pin #1= +2.2V Min	#3 = Oscillation
Pin #1 = 0.8V Max	#3 High Impedance