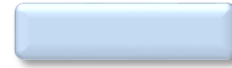


The ECS-200X Series clock oscillator offers low current drain and is compatible with HCMOS/LSTTL logic. It is ideal for low power HCMOS applications. The metal package with pin #7 case ground acts as shielding to minimize radiation.



OPERATING CONDITIONS / ELECTRICAL CHARACTERISTICS



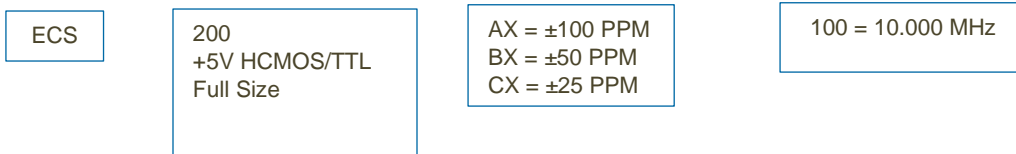
- HCMOS/LSTTL logic compatible
- Wide frequency range
- Low power consumption
- Resistance weld package
- PbFree/RoHS Compliant

Parameters	Frequency Range	Conditions	MIN	TYP	MAX	Units
Frequency (F_O)	1.000 ~ 150.000		1.000		150.000	MHz
Operating Temperature Range (T_{OPR})	1.000 ~ 150.000		0		+70	°C
Storage Temperature Range (T_{STG})	1.000 ~ 150.000		-55		+125	°C
Frequency Stability	1.000 ~ 150.000	All Conditions*	-100		+100	PPM
Input Current (I_{DD})	1.000 ~ 20.000				12	mA
	20.000 ~ 25.000				15	mA
	25.000 ~ 150.000				30	mA
Output Symmetry		50% V _{DD} level	45	50 ± 3	55	%
Rise Time (T_R)	1.000 ~ 25.000	10% ~ 90% V _{DD} level			10	nS
	25.000 ~ 150.000	10% ~ 90% V _{DD} level			5	nS
Fall Time (T_F)	1.000 ~ 25.000	90% ~ 10% V _{DD} level			10	nS
	25.000 ~ 150.000	90% ~ 10% V _{DD} level			5	nS
Output Voltage (V _{OL}) (V _{OH})	1.000 ~ 150.000	I _{OL} = 4 mA			0.5	V
	1.000 ~ 150.000	I _{OH} = -4 mA	4.5			V
Output Current (I _{OL}) (I _{OH})	1.000 ~ 150.000	V _{OL} = 0.5V			4	mA
	1.000 ~ 150.000	V _{OH} = 4.5V			-4	mA
Output Load	1.000 ~ 3.500	HCMOS/LSTTL			15	pF
	3.510 ~ 150.000	HCMOS/LSTTL			50	pF
Start-Up Time (T_S)	1.000 ~ 25.000				5	mS
	25.000 ~ 150.000				10	mS
Supply Voltage	1.000 ~ 150.000		+4.75	+5.0	+5.25	V _{DC}

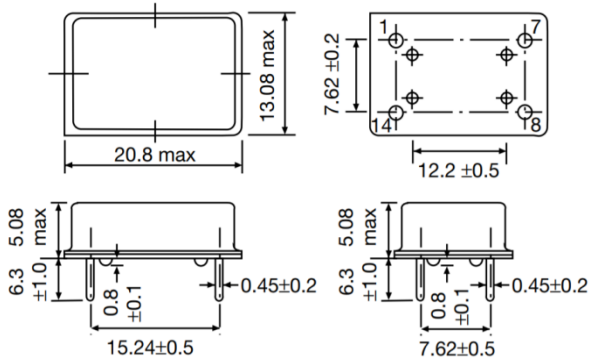
* Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration.

Part Numbering Guide: Example ECS-200AX-100

ECS - Series - Stability - Frequency Abbreviations



Package Dimensions (mm)



Pin Connections	
#1	NC
#7	Case GND
#8	Output
#14	+5 V DC

Figure 3) Pin Connections

Figure 1) Top, Side and Bottom views

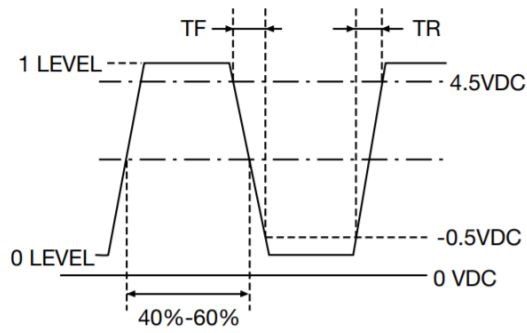


Figure 2) Output Wave Form